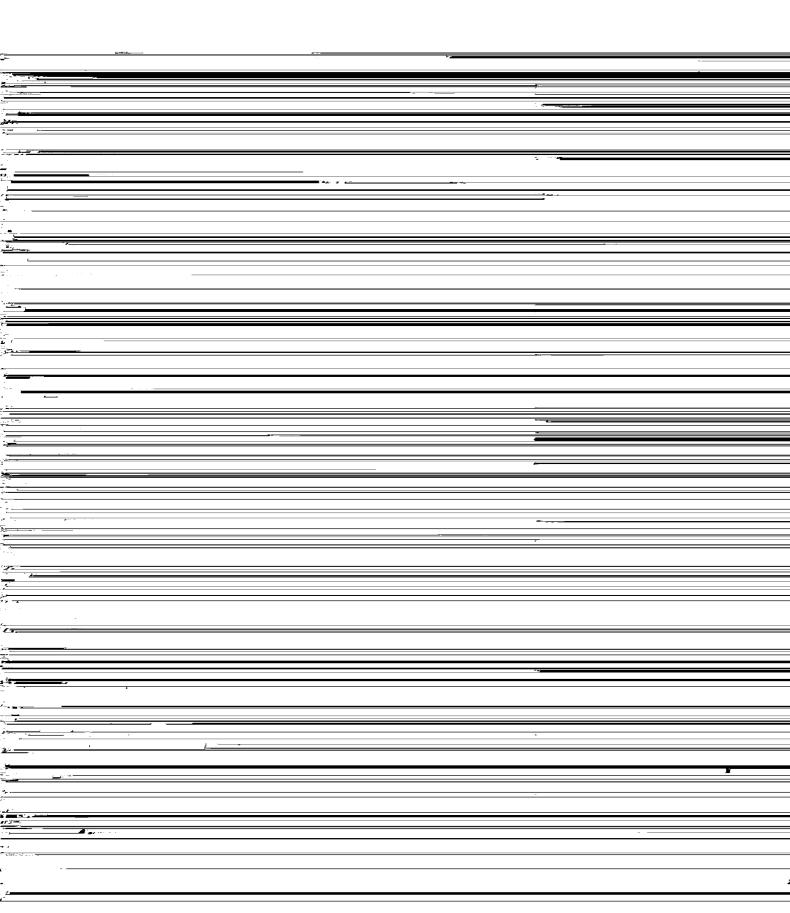
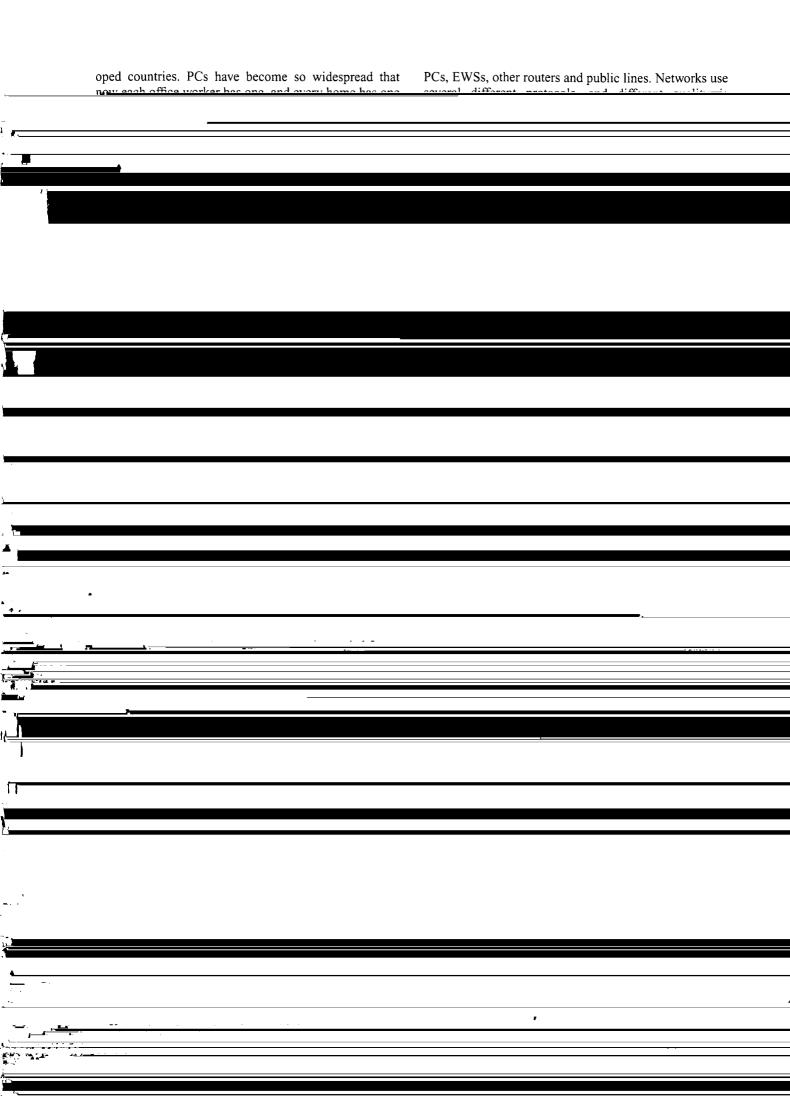
LSIs and Equipment for High Speed and Diversified Digital Communication Network*





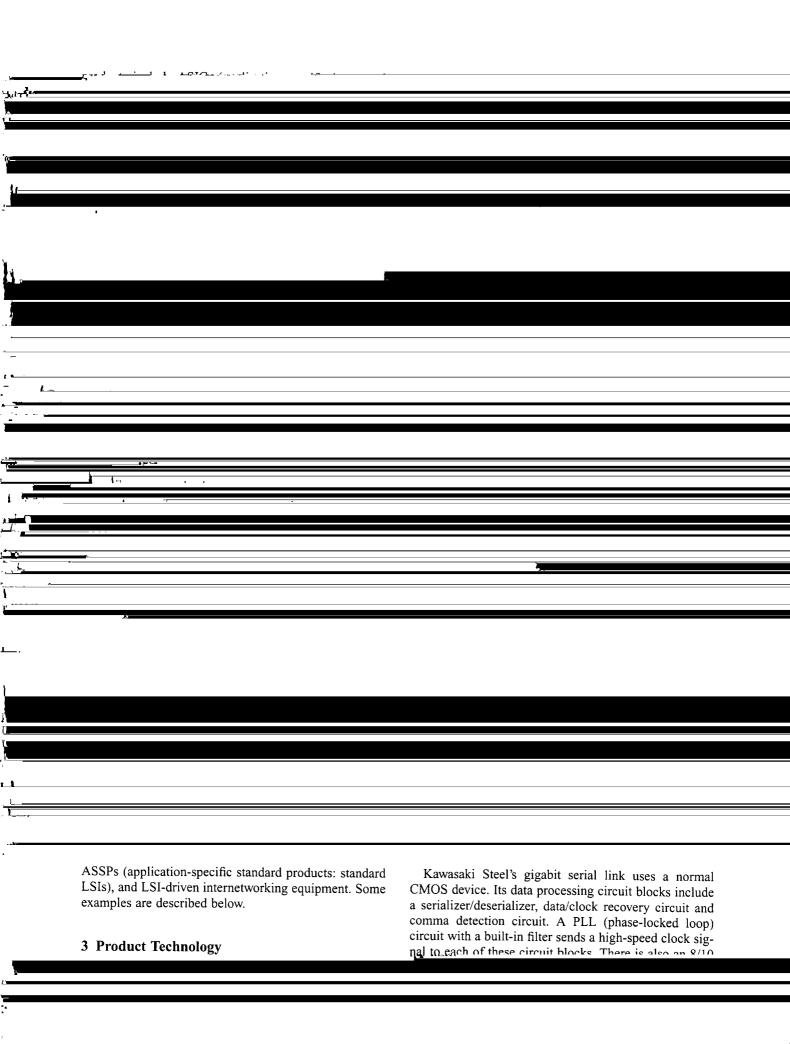


Table 3 TCP/IP engine

Realized technology

Performance

Table 4 Router

Performance

Realized technology

	Performance	Realized technology		Performance	Realized technology	
	· RFC1122 Host requirement	Parallel and separate processing	Secured A2DIS	Layer 3 switching	· Dedicated ASIC for	
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